

***Amendments to the Claims***

1. (previously presented) A radio frequency identification (RFID) integrated circuit (IC), comprising:

a first antenna pad;

a second antenna pad;

a first modulator coupled to said first antenna pad, wherein said first modulator is configured to backscatter modulate a first symbol received from said first antenna pad with a response symbol, wherein said first modulator is configured to output said backscatter modulated first symbol to said first antenna pad;

a second modulator coupled to said second antenna pad, wherein said second modulator is configured to backscatter modulate a second symbol received from said second antenna pad with the response symbol, wherein said second modulator is configured to output said backscatter modulated second symbol to said second antenna pad;

a first charge pump coupled to said first antenna pad, said first charge pump configured to convert a first high frequency signal from said first antenna pad to a first substantially direct current (DC) voltage; and

a second charge pump coupled to said second antenna pad, said second charge pump configured to convert a second high frequency signal from said second antenna pad to a second substantially direct current (DC) voltage.

2. (original) The RFID IC of claim 1, further comprising:

a first receiver coupled to said first antenna pad, wherein said first receiver receives said first symbol from said first antenna pad and outputs a first received signal; and

a second receiver coupled to said second antenna pad, wherein said second receiver receives said second symbol from said second antenna pad and outputs a second received signal.

3. (original) The RFID IC of claim 2, further comprising:

a state machine configured to receive said first received signal and said second received signal and generate said response symbol.

4. (original) The RFID IC of claim 3, further comprising:

a data programming unit that stores a tag identification number.

5. (original) The RFID IC of claim 4, wherein said state machine is configured to read said stored tag identification number, wherein said state machine is configured to generate said response symbol using said first received signal, said second received signal, and a bit of said read tag identification number.

6-10. (canceled)

11. (previously presented) A radio frequency identification (RFID) tag device, comprising:

a first antenna;

a second antenna;

a first receiver coupled to said first antenna, wherein said first receiver is configured to receive a first symbol from said first antenna, and wherein said first receiver is configured to generate a first received signal;

a first modulator coupled to said first antenna, wherein said first modulator is configured to backscatter modulate the received first symbol with a response symbol, wherein said backscatter modulated first symbol is output to said first antenna;

a first charge pump coupled to said first antenna, said first charge pump configured to convert a first high frequency signal from said first antenna to a first substantially direct current (DC) voltage;

a second receiver coupled to said second antenna, wherein said second receiver is configured to receive a second symbol from said second antenna, and wherein said second receiver is configured to generate a second received signal;

a second modulator coupled to said second antenna, wherein said second modulator is configured to backscatter modulate the received second symbol with the response symbol, wherein said backscatter modulated second symbol is output to said second antenna;

a second charge pump coupled to said second antenna, said second charge pump configured to convert a second high frequency signal from said second antenna to a second substantially direct current (DC) voltage; and

a state machine coupled to said data programming unit and said first and said second receivers.

12. (original) The RFID tag device of claim 11, wherein said state machine is configured to receive said first received signal from said first receiver and to receive said second received signal from said second receiver, wherein said state machine generates said response symbol.

13. (original) The RFID tag device of claim 12, further comprising:  
a data programming unit that stores a tag identification number.

14. (original) The RFID tag device of claim 13, wherein said state machine is configured to read said stored tag identification number, wherein said state machine is configured to generate said response symbol using said received signal and a bit of said read tag identification number.

15-20. (canceled)

21. (previously presented) The RFID IC of claim 1, further comprising:  
a node at which said first substantially DC voltage signal and said second substantially DC voltage signal are coupled together.

22. (previously presented) The RFID IC of claim 21, further comprising:  
a power bus coupled to said node to route power to one or more components of the RFID IC.

23. (previously presented) The RFID IC of claim 1, further comprising:

means for storing a bit pattern.

24. (previously presented) The RFID tag device of claim 11, further comprising:

a node at which said first substantially DC voltage signal and said second substantially DC voltage signal are coupled together.

25. (previously presented) The RFID tag device of claim 24, further comprising:

a power bus coupled to said node to route power to at least said state machine.

26. (previously presented) The RFID tag device of claim 11, further comprising:

means for storing a bit pattern.

27. (previously presented) The RFID tag device of claim 24, further comprising:

means for communicating with a reader using a binary traversal algorithm, said binary traversal algorithm comparing bits within the bit pattern to symbols received from said reader.

28. (previously presented) The RFID tag device of claim 11, further comprising:

a substrate having an input capable of receiving a high frequency signal;

wherein said first antenna and said second antenna are disposed on said substrate.

29. (new) The RFID IC of claim 1, further comprising:

a first data recovery circuit coupled to said first antenna pad, said first data recovery circuit configured to demodulate said first high frequency signal to a first demodulated signal; and

a second data recovery circuit coupled to said second antenna pad, said second data recovery circuit configured to convert said second high frequency signal to a second demodulated signal.

30. (new) The RFID IC of claim 29, wherein said first data recovery circuit comprises a first demodulator circuit configured to detect an amplitude envelope of said first high frequency signal, and wherein said second data recovery circuit comprises a second demodulator circuit configured to detect an amplitude envelope of said second high frequency signal.

31. (new) The RFID IC of claim 29, wherein said first demodulated signal and said second demodulated signal are combined according to a logical OR function to output a digital information signal.

32. (new) The RFID IC of claim 29, wherein said first demodulated signal and said second demodulated signal are combined as analog signals.